

## Fairchild adds high end op amps

Fairchild Semiconductor is entering the high-performance amplifier market with five devices. The 210MHz FHP3x50 and the 50MHz FHP3x30 families allow designers to target a broad range of high definition and standard definition video applications including set-top boxes, digital TVs and audio/video amplifiers.

These two product families were developed with Fairchild's BCP6T technology, an ultra-low capacitance, complementary bipolar process using silicon-on-insulator (SOI) and deep trench isolation (DTI) technologies.

The triple- and quad-channel FHP3x50 devices were designed to optimise bandwidth in high-speed applications such as HD set-top boxes. These 210MHz full-power bandwidth (-3dB) op amps exceed HD video requirements and deliver 1100V/ $\mu$ s slew rate for optimal pulse response. In addition, the devices' 0.07%/0.03° differential gain/phase error improves video quality.

The devices in this family achieve optimal AC performance with a gain flatness of 0.1dB (50MHz), enabling them to maintain consistent gain over the entire passband of high-definition video signals. DC accuracy is also improved due to the 0.05 $\mu$ A typical input bias-current, which allows for more precise output-voltage levels and minimal droop in AC-coupled applications.

Both devices in the 210MHz family operate with just 3.6mA of supply current per amplifier, with the triple-channel device offering a disable feature to prevent additional power consumption. Energy-saving features like these, combined with an excellent output current rating ( $\pm$ 55mA), make these devices suitable for driving multiple video loads. The FHP3x50 products are also well suited for use in DTVs, audio/video amps and projectors and DVD recorders.

The single-, dual- and quad-channel devices in the FHP3x30 family provide 50MHz band-

width, 110V/ $\mu$ s slew rate and 0.008%/0.01° differential gain/phase to improve performance and save power in SD video designs. Targeted applications include SD set-top boxes, DTVs, CCD imaging systems and industrial imaging systems. Specifically, the FHP3230 device provide bandwidth of 170mHZ, output current of 100mA and DC performance 100dB at the lowest power consumption of 2.5mA).

Like the FHP3x50 series, the three amplifiers in the FHP3x30 family also provide superior gain flatness (16MHz to 0.1dB) to increase system reliability and simplify designs. These devices operate with a 2.5mA supply current per amplifier while providing  $\pm$ 100mA output capability to effortlessly support four video loads. Open loop gain, power supply rejection ratio, and common-mode rejection ratio are all better than 100dB.

**Fairchild Semiconductor**  
www.fairchildsemi.com

## Tool eases auto network design & test

The latest version of the DENoe/DENalyzer.J1587 from Vector simplifies design and testing of SAE J1587 networks in commercial vehicles. SAE J1587 is a standardized communication-protocol based on a serial, bidirectional SAE J1708 network. The standard regulates communication and data exchange between different ECUs.

The newly integrated Trouble Code Monitor simplifies work with the J1587 diagnostic protocol for users. This means that trouble codes can be displayed, and even queried if necessary, without additional programming effort. The tool allows the user to adjust the maximum wait time between two transmitted bytes over a large tolerance range.

**Vector Informatik**  
www.vector-informatik.com

## Viewer for Altium projects

The latest release of the Altium Designer Viewer Edition enables users to view, print, cross probe and explore design projects and documents that have been created in the Altium Designer unified electronic product development system. This license provides users with expanded functionality to support the recent Altium Designer 6 release as well as design projects created in all previous versions of the software.

Designed to be installed by all those in a company who are involved in the wider electronic product development process, the Viewer gives users ability to interrogate all aspects of a design while preserving the integrity of the projects by preventing editing of the design documents.

As a true viewer, it provides read-only access to a design, allowing them to review and provide feedback to the original Designer.

Users can open and securely explore single documents, design projects or multiple projects that have been grouped together as part of a design workspace.

All of Altium Designer's review and inspection capabilities are enabled in this latest Viewer Edition license providing engineering design team members with access to the full complement of query, viewing and inspection capabilities, from checking the parameters in a schematic component, to examining the PCB design rules, through to scrutinizing the routing with Altium Designer 6's new Board Insight Lens feature.

Users can also generate reports and printouts, and open and view Gerber, ODB++, NC Drill, and other manufacturing files with the Viewer Edition.

**Altium**  
www.altium.com



**Crossware has enhanced its ARM Development Suite by adding support for the Philips LPC210X ARM-based microprocessors via a combination of wizards, simulation, debugging, compiler extensions and pre-configuration.**

A set of Code Creation Wizards are provided for all on-chip peripherals and are able to generate configuration code, interrupt routines and, for the UART, complete I/O handlers. The simulator simulates the ARM core itself as well as many of the LPC210X on-chip peripherals including the vectored interrupt controller (VIC). This allows a developer to use the simulator to immediately test the code created using the wizards. The simulator can also be extended using the Virtual Workshop Interface that Crossware originally introduced for its 8051 microcontroller thereby allowing developers to construct a simulation of their complete target system.

The jPOD USB JTAG interface to facilitate on-chip debugging and which connects to the standard ARM 20-pin JTAG connector allowing the Crossware source level debugger to drive the on-chip ARM embedded in-circuit emulator (EmbeddedICE) logic.

**Crossware** www.crossware.com